

Curso de CAD electrónico basado en Orcad 9.2

Lógica positiva: $Y = \overline{A \cdot B}$

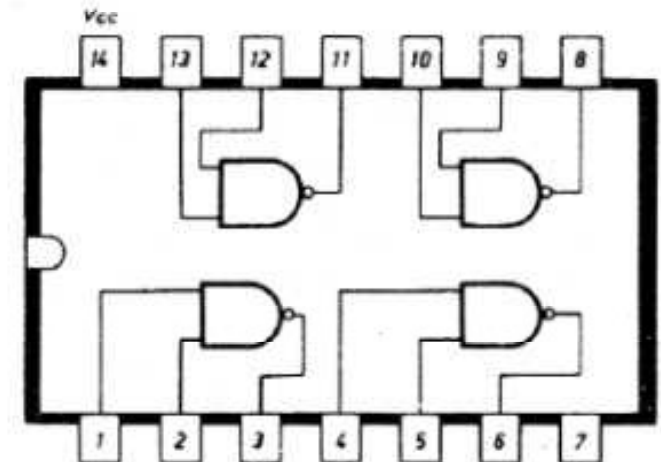
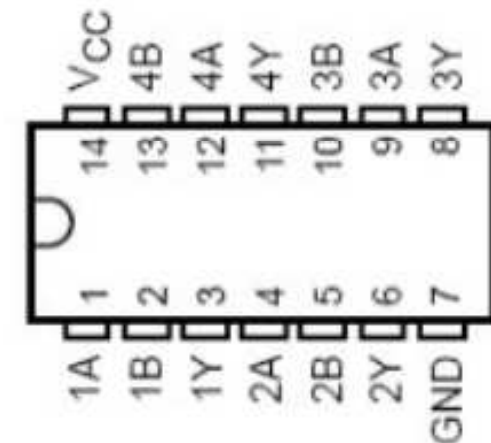


TRUTH TABLE

A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1, 4, 9, 12	1A to 4A	Data Inputs
2, 5, 10, 13	1B to 4B	Data Inputs
3, 6, 8, 11	1Y to 4Y	Data Outputs
7	GND	Ground (0V)
14	V _{CC}	Positive Supply Voltage



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